

FIG.1

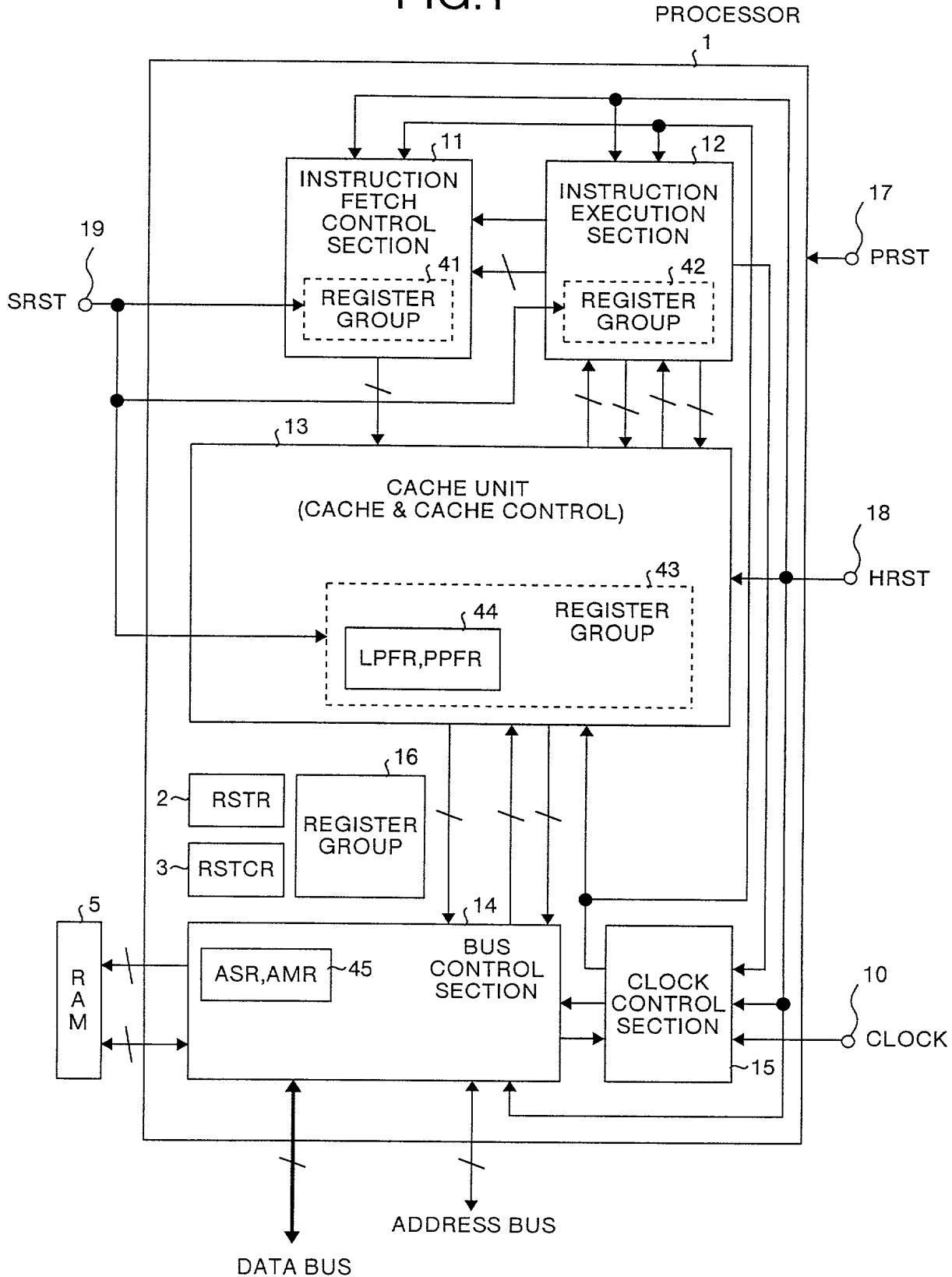


FIG.2

REGISTER ABBREVIATION	REGISTER NAME (INITIAL VALUE)
ARS0	DRAM ADDRESS AREA SETTING REGISTER 0
ARS1	DRAM ADDRESS AREA SETTING REGISTER 1
ARS2	DRAM ADDRESS AREA SETTING REGISTER 2
ARS3	DRAM ADDRESS AREA SETTING REGISTER 3
AMK0	DRAM ADDRESS MASK REGISTER 0
AMK1	DRAM ADDRESS MASK REGISTER 1
AMK2	DRAM ADDRESS MASK REGISTER 2
AMK3	DRAM ADDRESS MASK REGISTER 3
DCTL	DRAM CONTROL REGISTER
DAMC	DRAM ACCESS MODE CONTROL REGISTER
DCFG	DRAM CONFIGURATION CONTROL REGISTER
DAN	DRAM ADDRESS NUMBER REGISTER
DSTS	DRAM STATUS REGISTER
DRCN	DRAM REFRESH CONTROL REGISTER
DART	DRAM AUTO REFRESH TIMER REGISTER
RSTR	RESET REGISTER

FIG.3

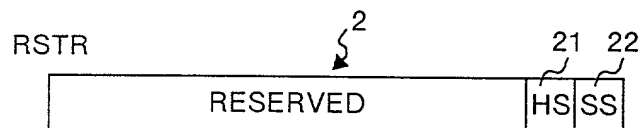


FIG.4

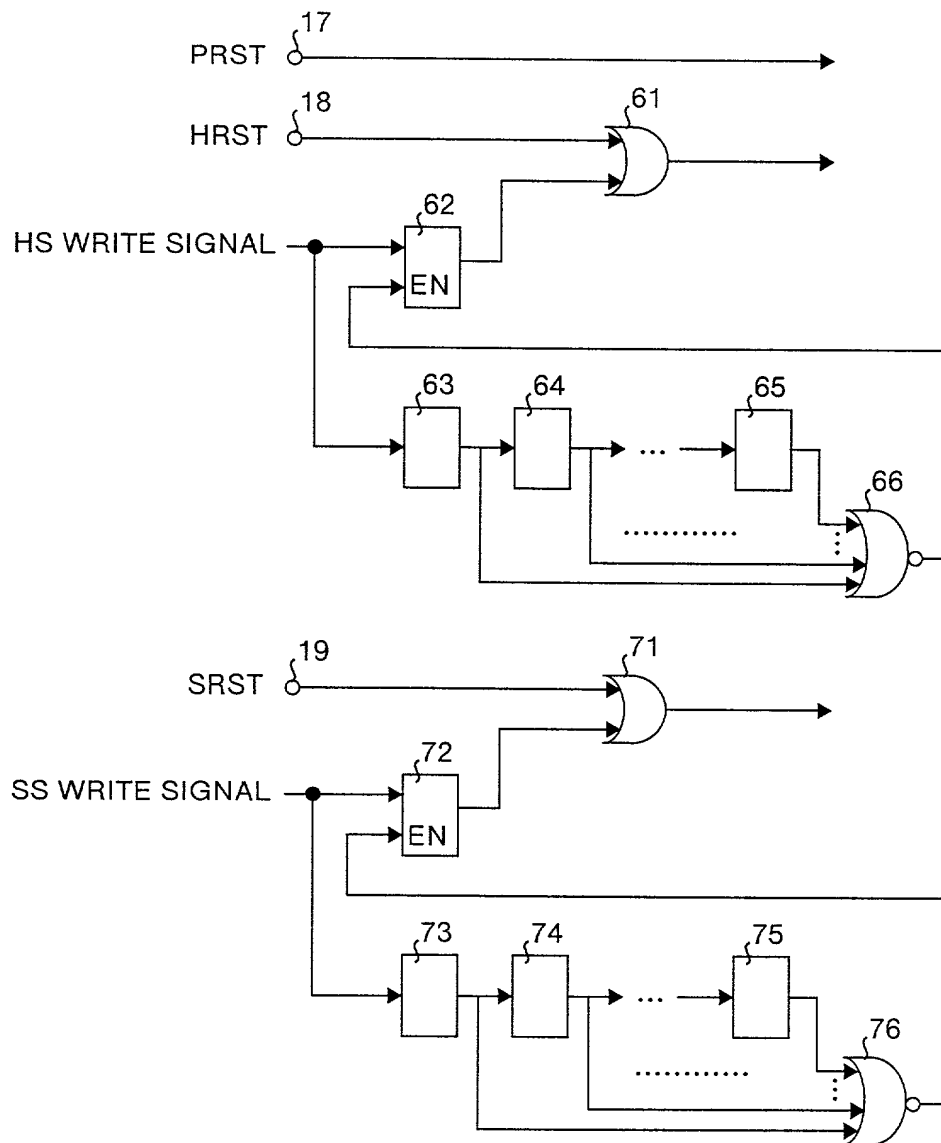


FIG.5

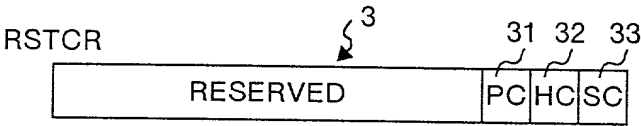


FIG.6

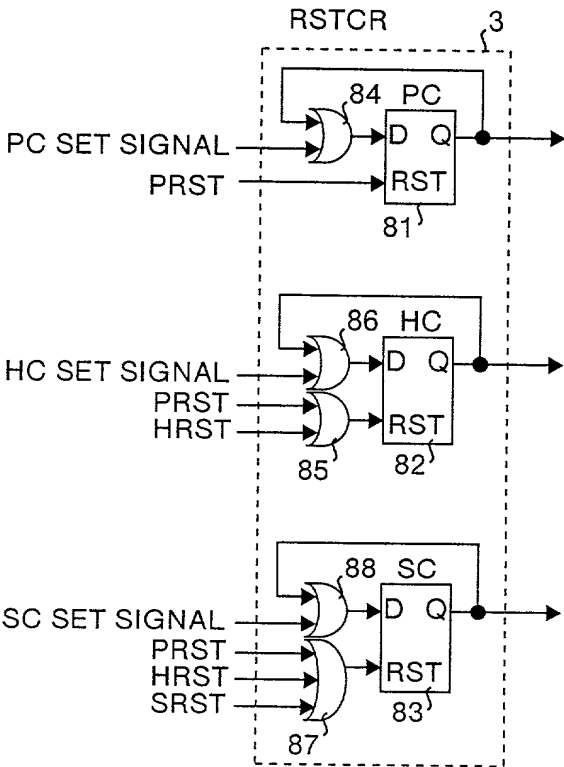


FIG.7

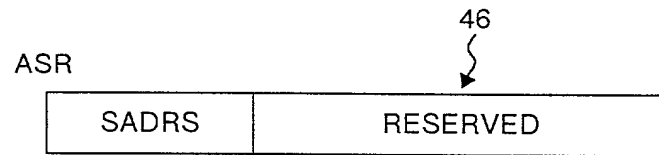


FIG.8

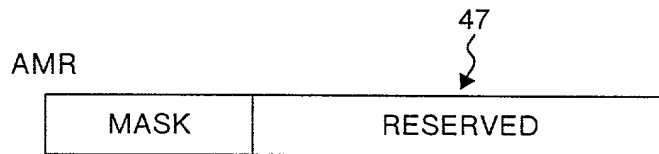


FIG.9

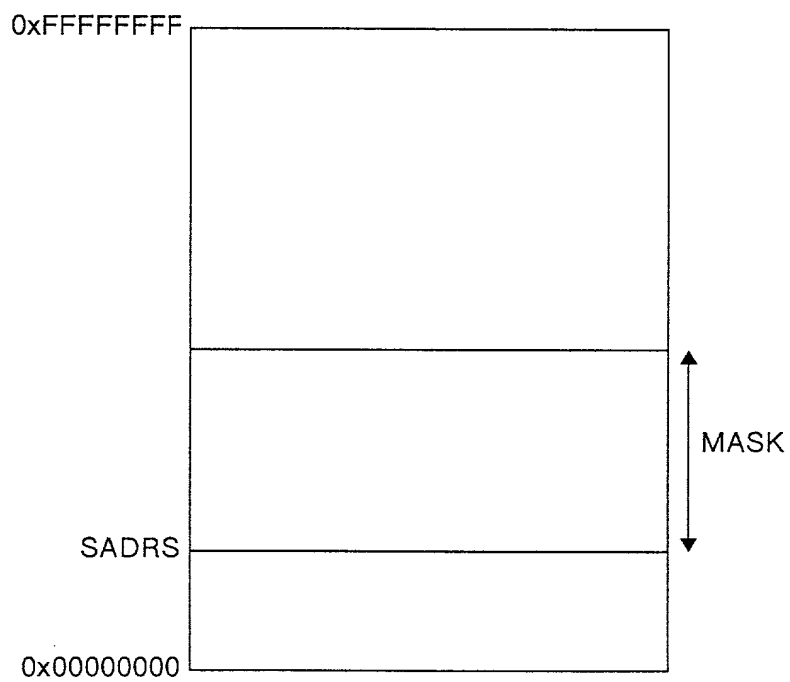


FIG.10

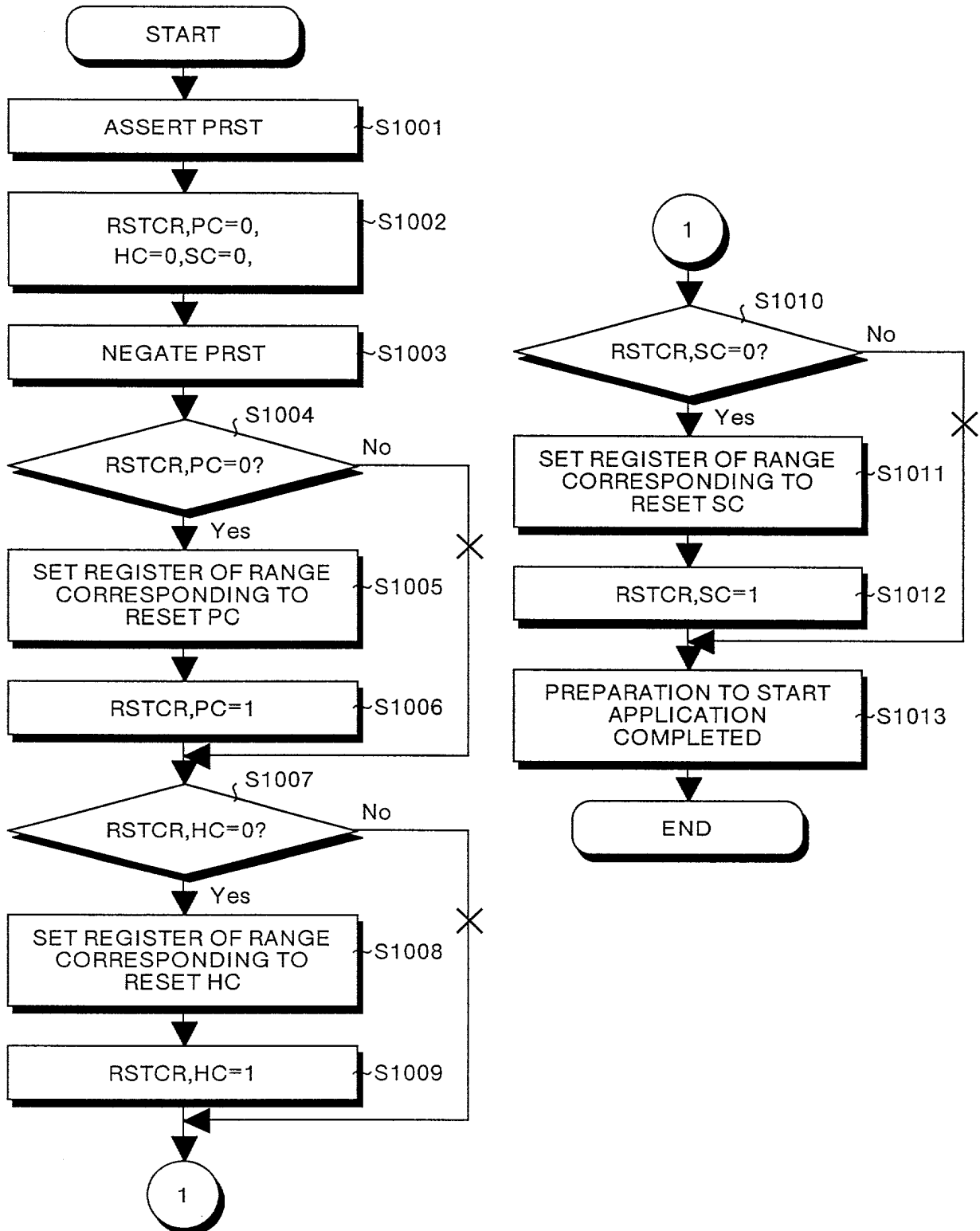


FIG.11

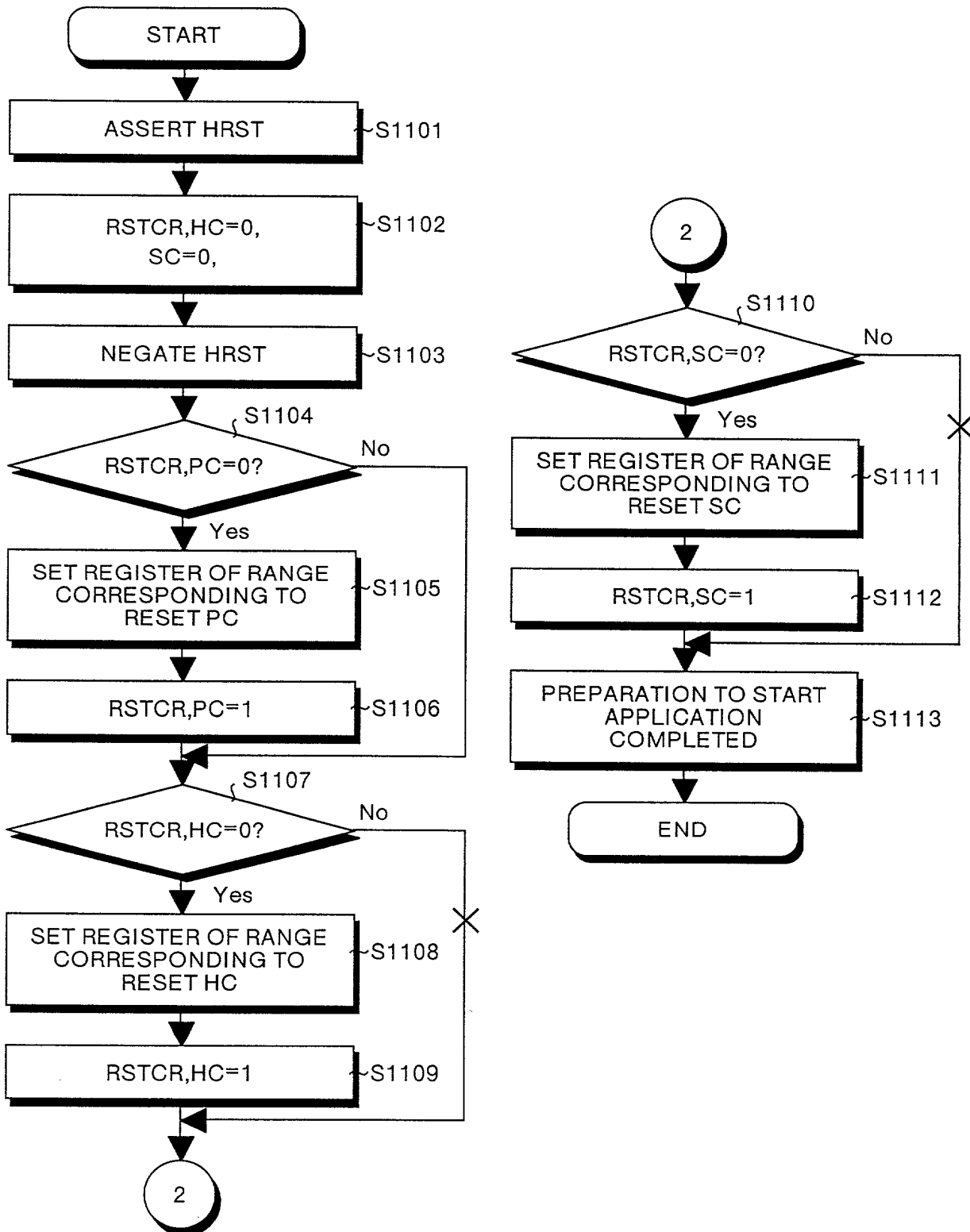


FIG.12

